











D2-230/240 Key Features



D2-240: for basic applications

The D2-240 provides a subset of the D2-250-1's capabilities. If you need a good CPU with multiple communications ports, and complex math or PID isn't required, then the D2-240 is the CPU for

Built-in memory

There is 2.5K of EEPROM program memory in the D2-240. No additional memory is required.

If you have critical data stored in the capacitor backed V-memory, simply purchase the optional lithium battery (D2-BAT) to permanently maintain these parameters as well.

Powerful instructions

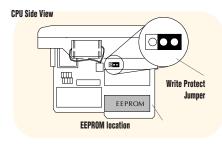
The D2-240 instructions cover most of the capability of our more powerful D2-250-1, and allow you to cover a wide variety of applications. Instructions include Boolean logic, data manipulation, integer math, interrupts, subroutines, FOR/NEXT loops, etc. For a complete list of instructions, see the back of this section.

Two built-in RS-232 communications ports

The D2-240 offers two communication ports. The top port can be used for a direct connection to a personal computer for programming, to our handheld programmer, C-more, or to the DV-1000. The bottom port is a slave-only port and supports our *Direct*NET[™] or K-sequence protocol at speeds up to 19.2 Kbaud. If you're using an operator interface or if you plan on connecting the system to a network later on, then you can choose the D2-240. The D2-240 also supports the D2-DCM Data Communication Module and the H2-ECOM Ethernet Communication Modules.

DL205 spare **EEPROM** chips

There may be cases where you want to have a spare EEPROM chip available. For example, maybe you need to upgrade a customer's machine with your latest enhancements. You can purchase extra EEPROM chips (two per pack). These can be installed in the CPU (D2-230/D2-240 only) and programmed, or they can be programmed directly with the DL205 handheld programmer.



EEPROM	D2-EE-1	D2-EE-2	
CPU	D2-230	D2-240	
CPU Program Storage Capacity	2.0K	2.4K	
Writing Cycle Life	10,000	10,000	
Write Inhibit	CPU jumper CPU jumper		
Memory Clear Method	Electrical	Electrical	



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C-more 8

other HM

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D2-230: our lowest price DL205 CPU

The D2-230 is our most economical CPU in the DL205 product family. If you are looking at the DL205 primarily because of the size, or for other reasons that don't require lots of CPU horsepower, then give the D2-230 a try.

Built-in EEPROM memory

There is 2.0K of EEPROM program memory in the D2-230. No additional memory is required.

If you have critical data stored in the capacitor-backed V-memory, simply purchase the optional lithium battery (D2-BAT) to permanently maintain these parameters as well.

One built-in communications port

The D2-230 has only one communication port. If you are considering any network connections in the future, you will need the D2-240, D2-250-1 or D2-260 CPU. The extra port may be worth the cost, especially during machine startup or troubleshooting sessions. The D2-230 does not support the Ethernet or Data Communications modules.

Basic instruction set

The D2-230 provides a subset of the D2-240's well-rounded instructions. The D2-230's instructions cover basic Boolean and simple integer math.

e4-31









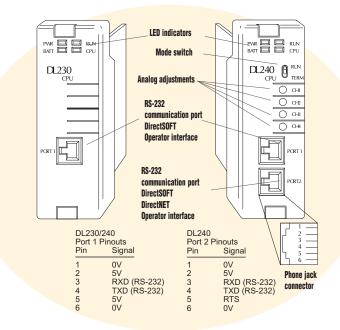


D2-230/240 Key Features

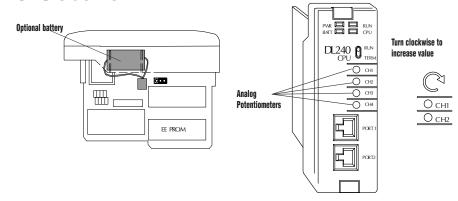
The diagram to the right shows the various hardware features found on the D2-230 and D2-240 CPUs.

	C	PU Status Indicators		
RUN	ON	CPU is in RUN mode		
NUIN	OFF	CPU is in PROGRAM mode		
BATT	ON	Battery backup voltage is low		
	OFF	Battery backup voltage is OK or disabled		
CPU	ON	CPU internal diagnostics detects error		
	OFF	CPU is OK		
PWR	ON	CPU power good		
	OFF	CPU power failure		
	Mod	de Switch (D2-240 only)		
RUN		Puts CPU into RUN mode		
TERM		Allows peripherals (HPP, <i>Direct</i> SOFT) to select the mode of operation		
		Port 1		
Protoc	ols	K-sequence slave		
Devices		Can connect w/HPP, <i>Direct</i> S0FT™, <i>C-more</i> , DV-1000		
Specs.		6P6C phone jack connector RS-232 9,600 baud Fixed address Odd parity only 8 data bits, one start, one stop asynchronous, half-duplex, DTE		
		Port 2 (D2-240 only)		
Protoc	ols	K-sequence slave, <i>Direct</i> NET slave		
Devices		Can connect w/many devices, such as PCs running <i>Direct</i> SOFT, DSData, HMI packages, <i>C-more</i> , DV-1000, or any <i>Direct</i> NET master		
Specs.		6P6C phone jack connector 300/600/1200/2400/4800 9600/19.2k baud Odd or no parity Selectable address (1-90, HEX 1 – 5A) 8 data bits, one start, one stop Asynchronous, Half-duplex, DTE		
		Battery (Optional)		
D2-BA	Т	CR14250SE		

Note: Batteries are not needed for program backup. However, you should order a battery if you have parameters in V-memory that must be maintained in case of a power outage.



CPU side view



Four external potentiometers for adjustments

There are four potentiometers on the face plate of the D2-240 CPU. They have a resolution of 256 steps and can be used to externally adjust four predefined V-memory locations inside the D2-240 CPU. You specify upper and lower limits for the values and the CPU takes care of the rest!

1 - 8 0 0 - 6 3 3 - 0 4 0 5 e4-32 **Programmable Controllers**













DL205 CPU Specifications

DL205 CPU Comparison							
System Capacity	D2-230	D2-240	D2-250-1	D2-260			
Total memory available (words) Ladder memory (words) V-memory (words) Battery backup Total CPU memory I/O pts. available (actual I/O pts. depend on I/O configuration method selected) Local I/O (pts.) Local Expansion I/O (pts.)	2.4K 2048 EEPROM 256 Yes 256 256 none	3.8K 2560 EEPROM 1024 Yes 896 (320 X + 320 Y + 256 CR) 256 none	14.8K 7680 Flash 7168 Yes 2048 (512 X + 512 Y + 1024 CR) 256 768 (2 exp. bases max) (Including local I/O)	30.4 15872 Flash 14592 Yes 8192 (1024 X + 1024 Y + 2048 CR + 2048 GY + 2048 GY) 256 1280 (4 exp. bases max.) (Including local I/O)			
Serial Remote I/O (pts.) Remote I/O channels I/O per remote channel Ethernet Remote I/O Discrete I/O pts. Analog I/O channels Remote I/O channels I/O per remote channel	N/A N/A N/A N/A N/A N/A N/A	896 max. (Including local I/O) 2 2048 (limited to 896) Yes 896 max. (Including local I/O) Map into V-memory Limited by power budget 16,384 (limited to 896)	2048 max. (Including local and exp.I/O) 8 (7+1 CPU port) 2048 Yes 2048 max. (Including local and exp.I/O) Map into V-memory Limited by power budget 16,384 (16 fully expanded H4-EBC slaves using V-memory and bit-of-word instructions)	8192 max. (Including local & exp. I/O) 8 (7+1 CPU port) 2048 Yes 8192 (Including local and exp.I/O) Map into V-memory Limited by power budget 16,384 (16 fully expanded H4-EBC staves using V-memory and bit-of-word instructions)			
Performance							
Contact execution (Boolean) Typical scan (1K Boolean)	3.3µs 4-6ms	1.4µs 10-12ms	0.61µs 1.9ms	0.61µs 1.9ms			
Programming and Diagnostics							
RLL Ladder Style RLL***/Flowchart Style (Stages) Run time editing Supports Overrides Variable/fixed scan Instructions Control relays Timers Counters Immediate I/O Subroutines For/Next loops Timed Interrupt Integer Math Floating-point Math Trigonometric functions Table Instructions PID Drum Sequencers Bit of Word ASCII Print Real-time clock/calender Internal diagnostics Password security System and user error log	Yes Yes/256 Yes No Variable 113 256 64 64 Yes No	Yes Yes/512 Yes Yes Yes Yes Variable 129 256 128 128 Yes	Yes Yes/1024 Yes Yes Yes Yes Variable 174 1024 256 128 Yes	Yes Yes/1024 Yes Yes Yes Yes Variable 231 2048 256 256 Yes			
Communications							
Built-in ports K-sequence (proprietary protocol) DirectNET TM Modbus RTU master/slave ASCII communications Maximum baud rate	Port 1 RS-232 Yes No No No 9600	Port 1 RS-232 and Port 2 RS-232 Yes Yes No No 19.2K port 2	Port 1 RS-232 and Port 2 RS-232/422 Yes Yes Yes OUT 38.4K port 2	Port 1 RS-232 and Port 2 RS-232/422/485) Yes Yes Yes IN/OUT 38.4K port 2			



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